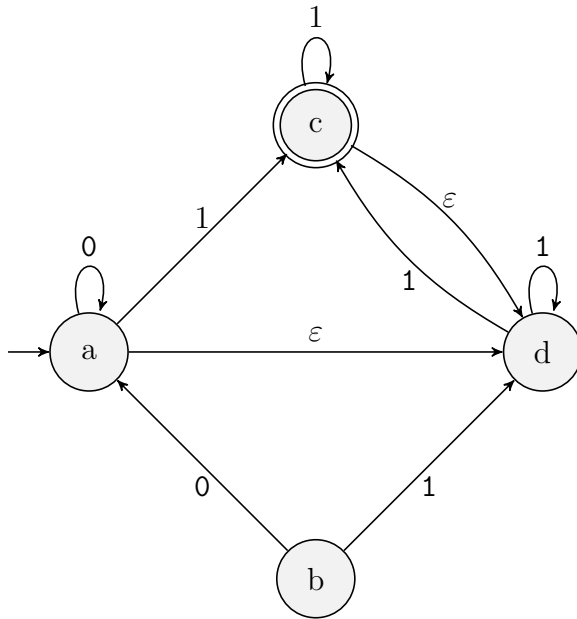


IMPORTANT: For each problem (LO1,LO2,GK,Advanced, All LO Makeups) write your solution using a **SINGLE** and **SEPARATE SHEET OF PAPER (BOTH FRONT AND BACK)**. Write your **FIRST NAME, ALPHABETIZING NAME** and **PROBLEM NUMBER** on each sheet.

25 Points Each

- LO1** a. Provide the state diagram for a DFA M that accepts exactly those words that contain an even number of 0's or (inclusive) contain exactly two ones. For example, 00100, 01010, and 1001 should all be accepted, but not 100101. (19 pts)
- b. Show the computation of M on i) 00100 and ii) 100101. (6 pts)

LO2 Do the following for the NFA N whose state diagram is shown below.



- a. Provide a table that represents N 's δ transition function. (12 pts)
- b. Use the table from part a to convert N to an equivalent DFA M using the method of subset states. Draw M 's state diagram. (8 pts)
- c. Show the computation of M on input $w = 11001$. (5 pts)
- GK** a. For the δ -transition function of a DFA M what are its inputs and what does it output (within the context of a computation)? How does an NFA's δ -transition function differ from that of a DFA's? (15 pts)
- b. Which of the following (there is only one) does *not* describe a regular language: i) all binary words that begin and end with the same number of 0's. ii) all binary words having a number of 1's that is divisible by 5, iii) all binary words that have the pattern 011001, iv) all binary words except for the word 1101. Defend your choice. (10 pts)

ADV Consider the alphabet

$$\Sigma = \left\{ \begin{pmatrix} 0 \\ 0 \end{pmatrix}, \begin{pmatrix} 0 \\ 1 \end{pmatrix}, \begin{pmatrix} 1 \\ 0 \end{pmatrix}, \begin{pmatrix} 1 \\ 1 \end{pmatrix} \right\}$$

where each symbol is a column vector of two bits. Provide the state diagram for a DFA M that accepts all words w over Σ for which the top and bottom layers of w represent binary words u and v , respectively, where v is equal to u shifted two bits to the right, with the first two bits of v filled in with 0's. Notice that, because of the shift, the last two bits of u will not appear in v . For example, the DFA should accept

$$w_1 = \begin{array}{l} 010101 \\ 000101 \end{array}$$

However, it should reject

$$w_2 = \begin{array}{l} 10001 \\ 10100 \end{array}$$

since the first bit of v should equal 0. Provide a description of each state of M . In other words, for each state $q \in Q$, what will be true about a word that forces M to terminate in state q ? (25 pts)